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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the Application of )  
DAN TOMESCU, et al. ) Examiner: Not Assigned  
on DATA PROCESSING SYSTEM ) Group Art Unit: 2183  
HAVING A CARTESIAN )  
CONTROLLER )  
Serial No.: 10/728,234 )  
Filed on: December 4, 2003 ) (Our Docket No. 5227-0001-1)  
Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sirs:

In accordance with 37 C.F.R. § 1.56, the references set forth below and listed on the attached forms PTO/SB/08A and PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited references are enclosed.

<u>U.S. Patent No</u>	<u>Entitled</u>	<u>Dated</u>
5,758,176	Method and System for Providing a Single-Instruction, Multiple-Data Execution Unit for Performing Single-Instruction, Multiple-Data Operations Within a Superscalar Data Processing System	05-26-1998

Intl Publ. No

Entitled

Dated

WO 01/53933

Split Embedded Dram Processor

07-26-2001

OTHER DOCUMENTS

SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study"  
Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987  
pages 239-258, XP000747239, ISSN: 0278-081X

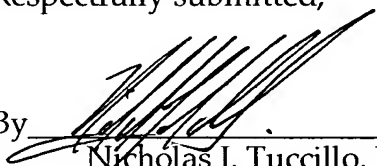
This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such a publication is prior art. Moreover, the Applicant understands that the Examiner is to make an independent evaluation of the cited information.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached forms PTO/SB/08A and PTO/SB/08B with initials or other appropriate marks.

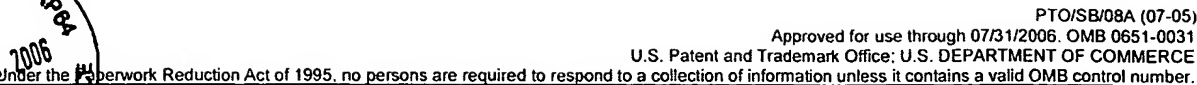
No fees are considered to be due; however, if it is determined that payment of a fee is required, please charge our deposit account No. 13-0235.

Respectfully submitted,

By

  
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Substitute for form 1449/PTO

*(Use as many sheets as necessary)*

Sheet	1	of	2
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**Complete if Known**

Application Number	10/728,234
Filing Date	December 4, 2003
First Named Inventor	DAN TOMESCU
Art Unit	2183
Examiner Name	
Attorney Docket Number	5227-0001-1

## U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	MM-DD-YYYY			
		WO 01/53933	07-26-2001	MICRON TECHNOLOGY		

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/728,234		
		Filing Date	December 4, 2003		
		First Named Inventor	DAN TOMESCU		
		Art Unit	2183		
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	5227-0002-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		SHAW D E et al: "Active Memory Chip: A Brief Survey And Case Study" Circuits, Systems And Signal Processing, Cambridge, MS, US, vol. 6 no. 2, 1987 pages 239-258, XP000747239,	

Examiner Signature		Date Considered	
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